

PATENT APPLICATION

Atty. Docket No.: 8750-019
 Applicant: Chang-Yong LEE, et al.
 Filing Date: November 13, 2001

Serial No. 10/002,542

Group: 2818

**INFORMATION DISCLOSURE CITATION
 FORM PTO-1449 (Modified)**

U.S. PATENT DOCUMENTS

Exam Init	Ref	Document Number	Issue Date	Name	Class	Sub- Class
TL		6,084,803	Jul 4, 2000	Sredanovic et al		

FOREIGN PATENT DOCUMENTS

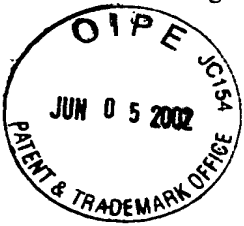
Document Number	Publication Date	Country	Name

OTHER DOCUMENTS

Exam Init	Ref	(Including Author, Title, Date, Pertinent Pages, etc.)

Examiner: Thong le
 Date Considered: 7/18/02

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OTHER DOCUMENTS

Exam Init	Ref	Author, Title, Date, Pertinent Pages, Etc.)
<u>TL</u>	—	Sato, et al. Fast Cycle Ram(FCRAM); a 20-ns Random Row Access, Pipe-Lined Operating Dram 1998 page 22-25
<u>TL</u>	—	Taguchi, et al. A 40-ns 64-Mb Dram with 64-b Parallel Data Bus Architecture November 1991 page 1493-1497

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